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NO. 158

P. 2

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Jamet Hollrah (Depositor's name)
Jamet Hollrah (Signature)
October 13, 2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/618,237	07/11/2003	Gang Zhang	188122003300	4647

TITLE OF INVENTION: ANALOG INTEGRATED CIRCUIT LAYOUT DESIGN

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES NO	\$750 1400	\$300	\$0	\$1700	10/13/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
BOWERS, BRANDON	2825	716-004000

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SAN JOSE, CALIFORNIA 95134

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Authorized Signature

Typed or printed name Robert E. Seheid

Date October 13, 2006

Registration No. 42,126

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Comments:

Inventor: Gang ZHANG et al.		Attv Docket No.: 188122003300	
Application No.: 10/618,237	Filing Date:	July 11, 2003	
Group Art Unit: 2825	Examiner:	B. Bowers	
Title: ANALOG INTEGRATED CIRCUIT LAYOUT DESIGN			
Documents Filed:			
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